Cuno's Open Source Hardware for Microsoft Gadgeteer

@tamberg





What

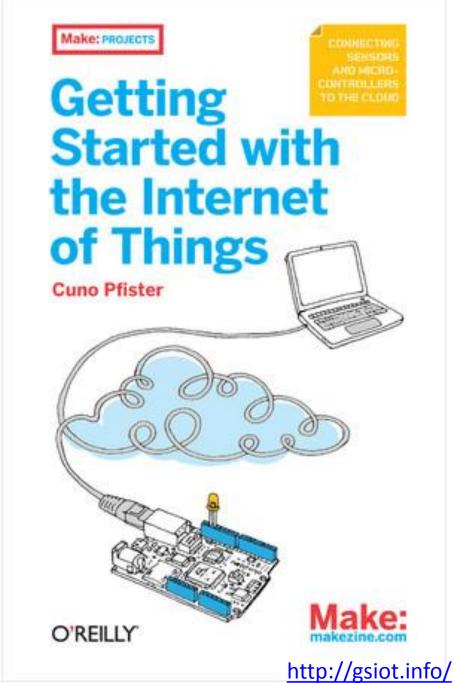
Cuno: My boss at **Oberon microsystems**, author of Getting Started with the Internet of Things

Gadgeteer: MS Research conventions to build independently extensible modular hardware

Mountaineer boards: Cuno's **open source hardware** mainboards for MS Gadgeteer





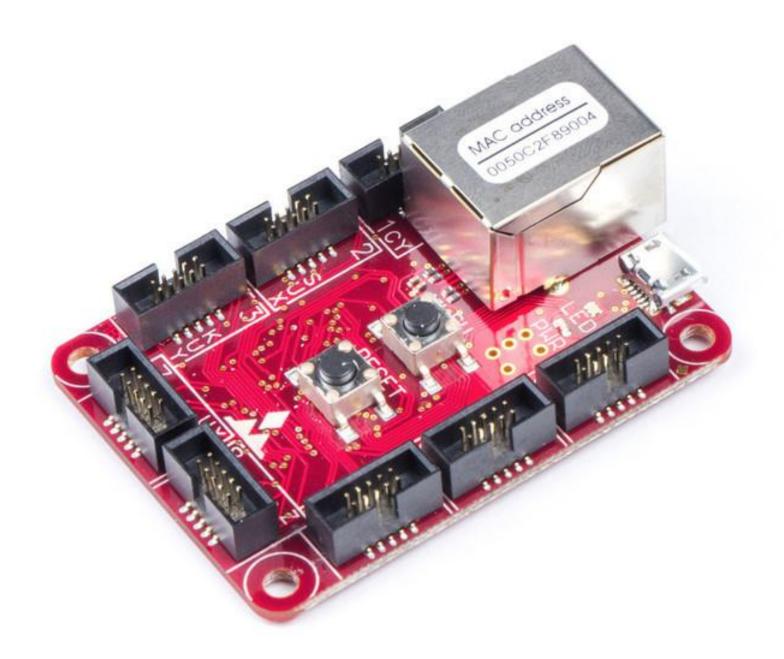


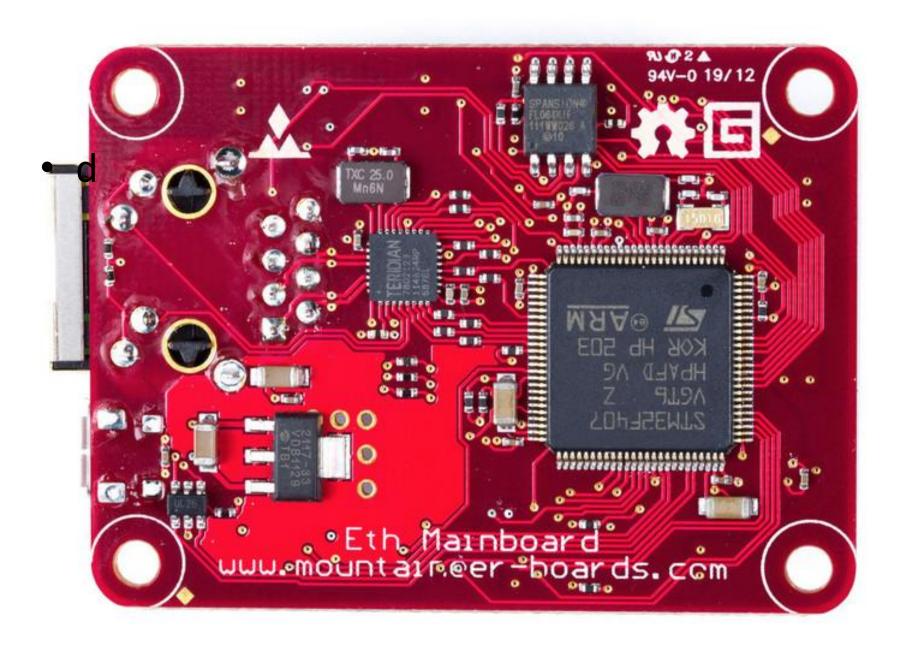
Application User Applications & Libraries Layer ClassLibrary Libraries .NET WPF Comms Layer Execution Type Garbage CLR Interop Engine Collector System Runtime Component PAL RAM 1/0 Timers Layer OS HAL Drivers or Facilities Processor and Peripherals Hardware

Layer



http://research.microsoft.com/en-us/projects/gadgeteer/





Why

Cuno: "needed own reference design anyway; OSHW simplified collaboration; nice marketing"

Beat (Oberon's Woz): "Technical challenge"

Stefan (CSA Engineering): "Cuno; demonstrating ARM/STM32 knowledge; prototyping platform; available personal resources at the time"



How



Together with CSA Engineering in Solothurn

Collaboration by handshake, no contracts

2010: NETMF port for expensive ARM dev board

2011: NETMF firmware published as Apache 2

2012: Board schematics and layouts, CC BY-SA



Results

First ARM **Cortex M port** of NETMF; 3 others failed trying; now used by GHI, Secret Labs (Netduino), others; Cuno and Beat became part of core NETMF developer team

Small Web shop for CH, EU; GHI as US-producer, licensee

Projects by Oberon, CSA, others, based on OSHW design

Italian company **InnovActive** became 3rd member of Mountaineer group (equally committed to quality)



Learned

Cuno: "Chip industry not easy to approach, ST is a positive exception; OSHW nice, still a mystery"

Beat: "Biggest pain was **bad documentation**, had to study C source examples for many hours"

Stefan: "Integrating IwIP stack was hard; we're a service company, had to learn to sell products"



More

http://mountaineer.org/

http://netmf.codeplex.com/

http://gadgeteer.codeplex.com/

http://goo.gl/7QcEH (NETMF for STM32 details)

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